



ASiKS-Engineering

User Manual

LIN-RS232 opt. Module

Document Reference No.: UM128730

Version 2.0

Issue Date: 2019-01-15

This document provides detailed information about ASiKS-Engineering's LIN-RS232 opt. Module.

**ASiKS-Engineering
Your Reliable Partner
in System Engineering, Prototyping, Hardware and Software Development
Dr. Andreas Schramm**

Pointenweg 3, 94110 Wegscheid, Germany
Tel.: +49(0)8165/4093868
E-Mail: info@asiks-engineering.com Web: <https://www.asiks-engineering.com>

Copyright © 2010 ASiKS-Engineering

TABLE OF CONTENTS

1 Introduction	3
2 Connectors and pinning description	3
2.1 LIN connector D-SUB 09 plug (male)	3
2.2 RS232 Connector D-SUB 09 plug (female)	4
3 Contact Information.....	4
Appendix A – Revision History	4

1 Introduction

The LIN-RS232 opt. Module shown in Figure 1 is used for easily connecting a LIN Bus to the serial Port (RS232) of e.g. a PC.

As it is passive module, no baud rate conversion or LIN Bus specific signal generation is done the module.



Figure 1: LIN-RS232 opt. Module

This interface provides the following features:

- 9-pin D-SUB plug (male) to connect to the LIN Bus
- 9-pin D-SUB plug (female) to connect to the serial port of e.g. a PC
- As the module implements galvanic separation, it needs to be powered on both sides
- Dimensions: 63mm x 34mm x 17mm
- Optimised to be used with LIN-EASYSTARTER (LinMon software, downloadable at https://www.asiks-engineering.com/downloads_eng.html)

2 Connectors and pinning description

2.1 LIN connector D-SUB 09 plug (male)

Table 1 shows the pinning, functional description and electrical characteristics of the LIN connector of ASiK-Engineering's LIN-RS232 opt. Module.

Pin	Symbol	Description	Min	Typ	Max	Unit
3	GND	Ground	0	0	0	V
7	LIN*	LIN bus line input/output	-27		+40	V
9	VBat	battery supply	5	12	18	V

Table 1: Quick reference data – LIN D-SUB 09 male

Note *: Master configuration (1k PullUp), slave configuration on

2.2 RS232 Connector D-SUB 09 plug (female)

Table 2 shows the pinning, functional description and electrical characteristics of the RS232 connector of ASiKS-Engineering's LIN-RS232 opt. Module.

Pin	Symbol	Description	Min	Typ	Max	Unit
5	GND	Ground	0	0	0	V
4	DTR*	Supply voltage input 1	5	12	18	V
7	RTS*	Supply voltage input 2	5	12	18	V
2	RXD**	Receive data output				
3	TXD**	Transmit data input				

Table 2: Quick reference data – RS232 D-SUB 09 female

Note *: *The interface can be driven on RS232 side using a straight 1:1 cable and setting DTR and RTS signals to high voltage level, which usually is represented by a logical "1" on software side. However it depends on the host's RS232 transceiver and the used cable length.*

Hence it is recommended to use a separate power supply to directly supply the interface on the PC side

Note **: *The electrical characteristics of the RS232 Standard apply.*

3 Disclaimer

For damage of any kind, arising by the employment of the LIN-RS232 opt. Module, no requirements can be made valid opposite the supplier!

4 Contact Information

ASiKS-Engineering
Dr. Andreas Schramm
Pointenweg 3
94110 Wegscheid
Germany
Tel: +49(0)8165/4093868
E-mail: info@asiks-engineering.com
Web Site URL <https://www.asiks-engineering.com>

Neither the whole nor any part of the information contained in, or the product described in this manual, may be adapted or reproduced in any material or electronic form without the prior written consent of the copyright holder. This product and its documentation are supplied on an as-is basis and no warranty as to their suitability for any particular purpose is either made or implied. ASiKS-Engineering will not accept any claim for damages howsoever arising as a result of use or failure of this product. Your statutory rights are not affected. This product or any variant of it is not intended for use in any medical appliance, device or system in which the failure of the product might reasonably be expected to result in personal injury. This document provides preliminary information that may be subject to change without notice. No freedom to use patents or other intellectual property rights is implied by the publication of this document.

Appendix A – Revision History

Revision History

- 1.0 Initial Release 23rd March, 2015
- 2.0 User manual rework 15th January 2019